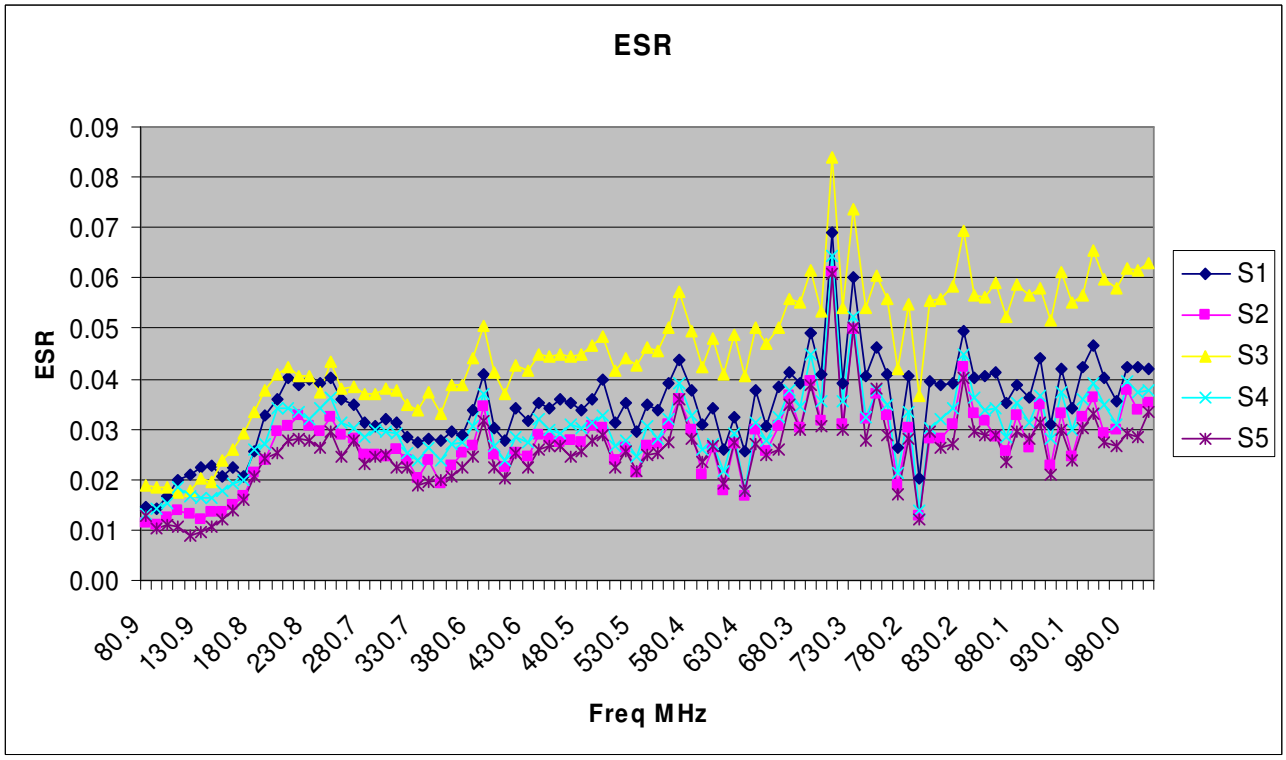
ESR and ESL Measurements of MVL3030Y 100nF Vertical Layer Capacitors

- Measurements were made from 1MHz to 1.0GHz using an HP 4291 Impedance Analyzer using the 16192A test fixture.
- The below plots will show ESR, "ESL effective", and "ESL calculated" where ESL calculated is the extracted inductance of the capacitor based off a simple RLC model. ESL effective is the reading taken directly from the impedance analyzer and assumes a simple RL (series R-L) model (i.e. the imaginary part of the measured impedance is assumed to represent a pure inductance).



Measured ESR for MVL3030Y 100nF Vertical Layer Capacitor. Five samples.

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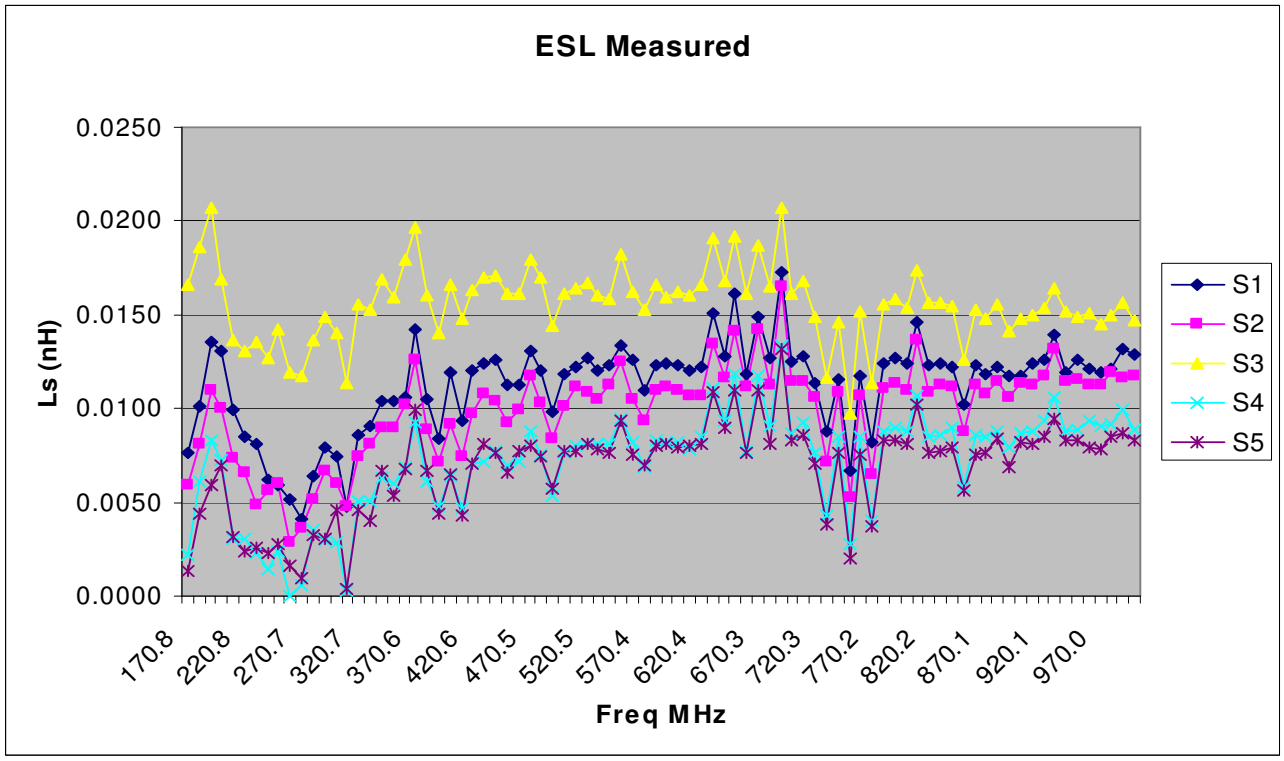
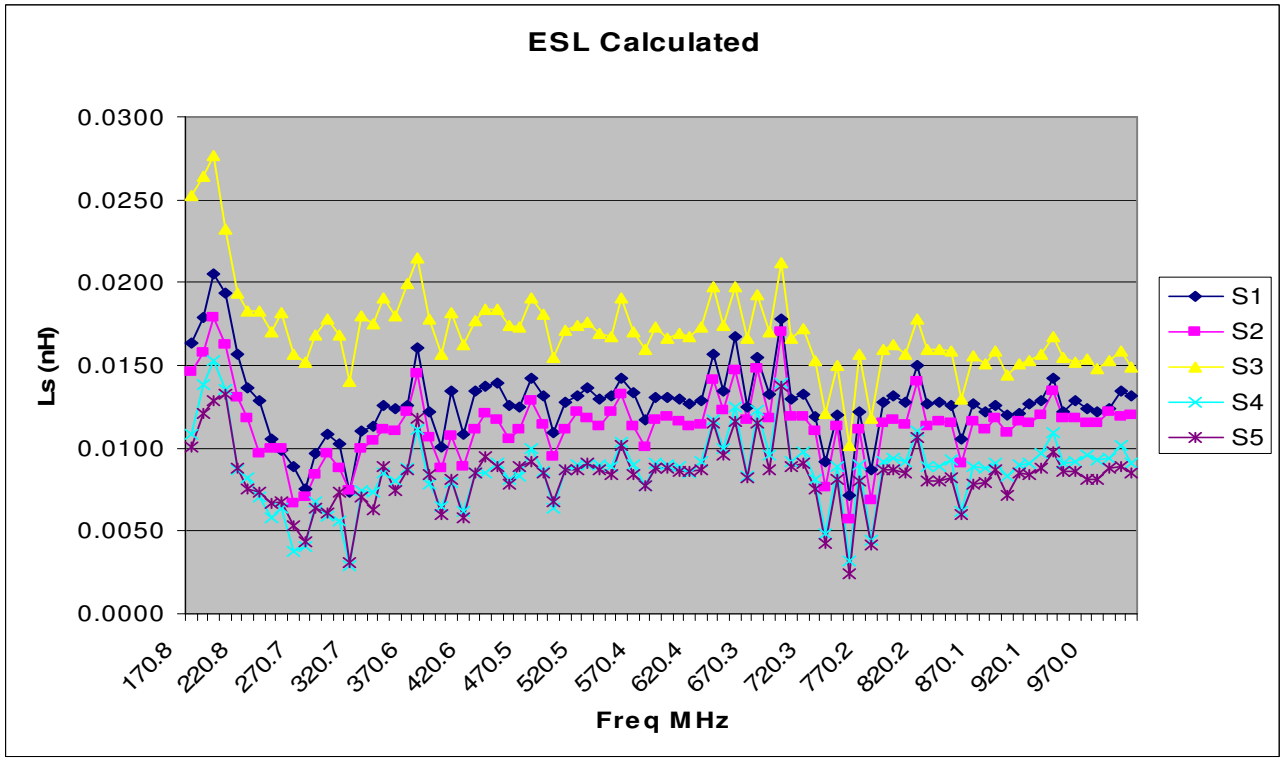


Measured ESL for MVL3030Y 100nF Vertical Layer Capacitor. Five samples.



Calculated ESL for MVL3030Y 100nF Vertical Layer Capacitor. Five samples.

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