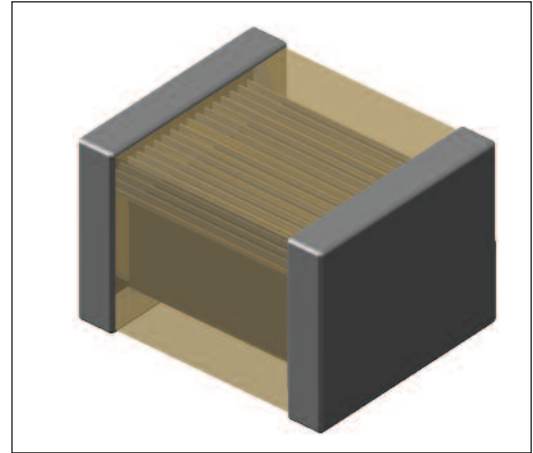


SURFACE MOUNT VERTICAL ELECTRODE™ CAPACITORS

PRESIDIO ADVANTAGE

- Ultra Lowest ESL
- Ultra Highest Q
- Ultra Combination of ESR and Q
- Ultra Stable NPO
- Ultra Stable Over Time — No Aging
- Ultra Compliant — Strontium Free
- Vertical Electrodes Built In
- Low Profile



DIELECTRIC SELECTION:

Porcelain NPO: Ultra temperature stable dielectric. Extremely low ESR. For RF power and high Q applications.

Porcelain P90: Dielectric increases over temperature (90ppm/C). Extremely low ESR. For temperature compensation.

Ceramic NPO: Ultra temperature stable dielectric. Low ESR. Suited for higher capacitance applications.

Ceramic BX: Temperature stable X7R dielectric. Suited for highest capacitance value applications.

TYPICAL APPLICATIONS:

Filter Capacitors

A filter design requires a specific capacitance value, cF, and at the upper end of the filter response, fF, the effective capacity must not exceed cF by more than a specifies amount of delta C. Once cF is determined, case size, voltage rating and temperature characteristics can be selected.

DC Block and RF Bypass

The bandwidth over which the insertion loss meets specification is determined by the location of parallel resonances. Minimum insertion loss at the band center is achieved by choosing a capacitor whose lowest series resonance is approximately at this frequency. Low impedance is typically more important than the capacitance value.

Low Noise Applications

Dissipation loss is the consideration. ESR is very small at the series resonance, very large at the parallel resonance. The neighboring parallel resonances determine the bandwidth.

